

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patentees: Alex Kalnitsky
Yih-Shung Lin

Patent No.: 5,986,330

Title: ENHANCED PLANARIZATION
TECHNIQUE FOR AN
INTEGRATED CIRCUIT

Issued: November 16, 1999

Atty Dk No.: 93-C-023C3



Reissue Application

Applicants: Alex Kalnitsky
Yih-Shung Lin

Serial No.: 09/998,595

Title: ENHANCED PLANARIZATION
TECHNIQUE FOR AN
INTEGRATED CIRCUIT

Filing Date: November 16, 2001

Atty Dk No.: 93-C-023RE (1678-42)

CERTIFICATE OF MAILING OR TRANSMISSION

I hereby certify that this correspondence is being deposited in the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Box MISSING PARTS, Washington, D.C. 20231, on this 22nd day of July, 2002.


Signature

REQUEST FOR EXTENSION OF TIME

July 22, 2002

TO THE ASSISTANT COMMISSIONER FOR PATENTS:

Applicant respectfully requests that the shortened statutory period for response to the outstanding Notice to File Missing Parts of Reissue Application mailed February 21, 2002, now set to expire on April 21, 2002 be extended three (3) months to expire on July 21, 2002 (Saturday).

Enclosed is our check no. 18997 in the amount of \$920.00 (months).

Please charge any additional fees or credit overpayment to Deposit Account
No. 07-1897.

Respectfully submitted,

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